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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/486,561

Filed: February 29, 2000



ATTN: Application Branch

Docket No.: 105030

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF,  
CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

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Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. No corrected filing receipt is requested in view of the fact that the errors will not be transferred to the Letters Patent upon its issuance under the Patent and Trademark Office policy announced at 1220 O.G.42 (March 9, 1999). Please contact the undersigned if there is any question about the accuracy of these corrections.

Respectfully submitted,

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Date: May 18, 2000

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/486,561	02/29/2000	2811	840	105030	7	15	2

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## Applicant(s)

NOBUAKI HASHIMOTO, SUWA-SHI, JAPAN;

## Continuing Data as Claimed by Applicant

THIS APPLICATION IS A 371 OF PCT/JP99/03418 06/25/1999

## Foreign Applications

JAPAN 10-201246 07/01/1998

If Required, Foreign Filing License Granted 05/10/2000

## Title

SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURE THEREOF, CIRCUIT BOARD, AND  
ELECTRONIC DEVICE

## Preliminary Class

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